

Abstract

Voltage regulation system

The invention relates to a voltage regulation process as well as to a voltage regulation system (11), with which a first voltage (VDD), present at an input (17) of the voltage regulating system (11), is changed into a second voltage (VINT), which can be tapped at an output (19c) of the voltage regulation system (11), with a first device (12, 13) for generating an essentially constant voltage (VBGR) from the first voltage (VDD), or a voltage derived from it, whereby a further device (34, 33) is provided in addition for generating a further voltage (VTRACK) from the first voltage (VDD) or a voltage derived from it, in particular a voltage (VTRACK) which can be higher than the voltage (VBGR) generated by the first device (12).

- Figure 2 -

Reference numbers

	1	Voltage regulation system
	2	Reference voltage generation device
5	3	Buffer circuit
	4	Voltage regulator
	5	Line
	6	Line
	7	Line
10	8	Line
	9a	Line
	9b	Line
	9c	Line
	11	Voltage regulation system
15	12	Reference voltage generation device
	13	Buffers circuit
	14	Voltage regulator
	15a	Line
	15b	Line
20	16a	Line
	16b	Line
	17	Line
	18	Line
	19a	Line
25	19b	Line
	19c	Line
	20	Differential amplifier
	21a	Plus input
	21b	Plus input
30	22	Field-effect transistor
	23	Line
	24	Line
	25	Line
	26	Line
35	27	Line /
	28	Differential amplifier
	29	Field effect transistor

29a Line
31 Minus input
32 Plus input
33 Buffer circuit
5 34 Reference voltage generation device
35 Register
36 Control line
37 Line
38 Line
10 39 Line
115a Line
115b Line
116b Line
116c Line
15 118 Line
120 Differential amplifier
121a Plus input
121b Plus input
122 Field-effect transistor
20 123 Line
124 Line
125 Line